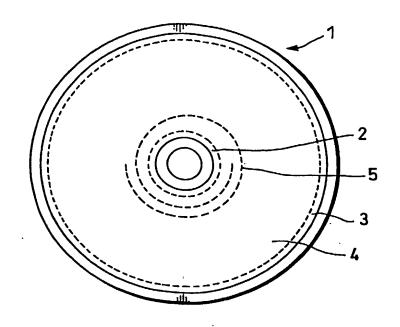
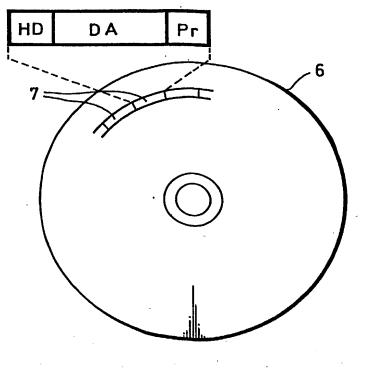
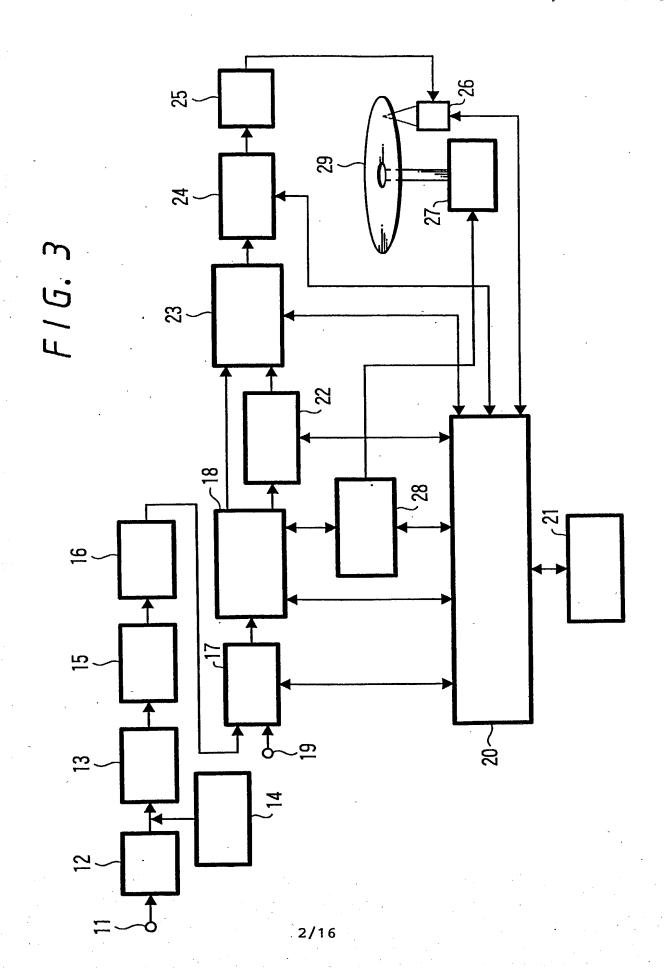
F / G. 1

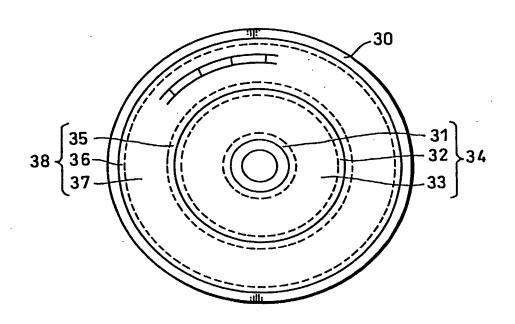


F I G. 2

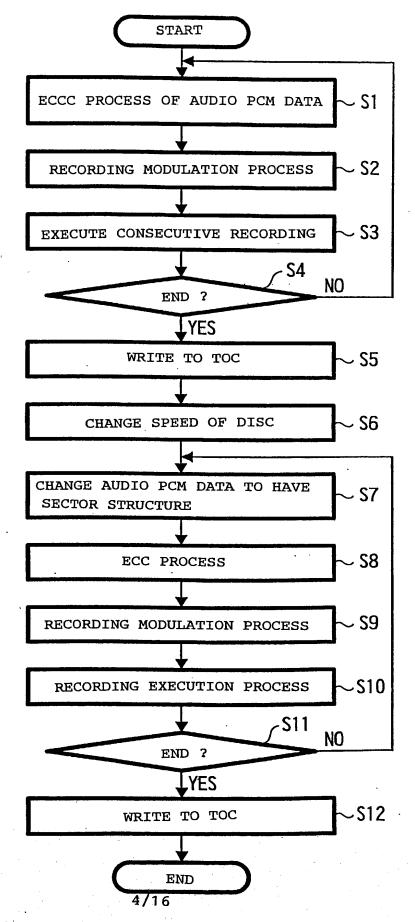


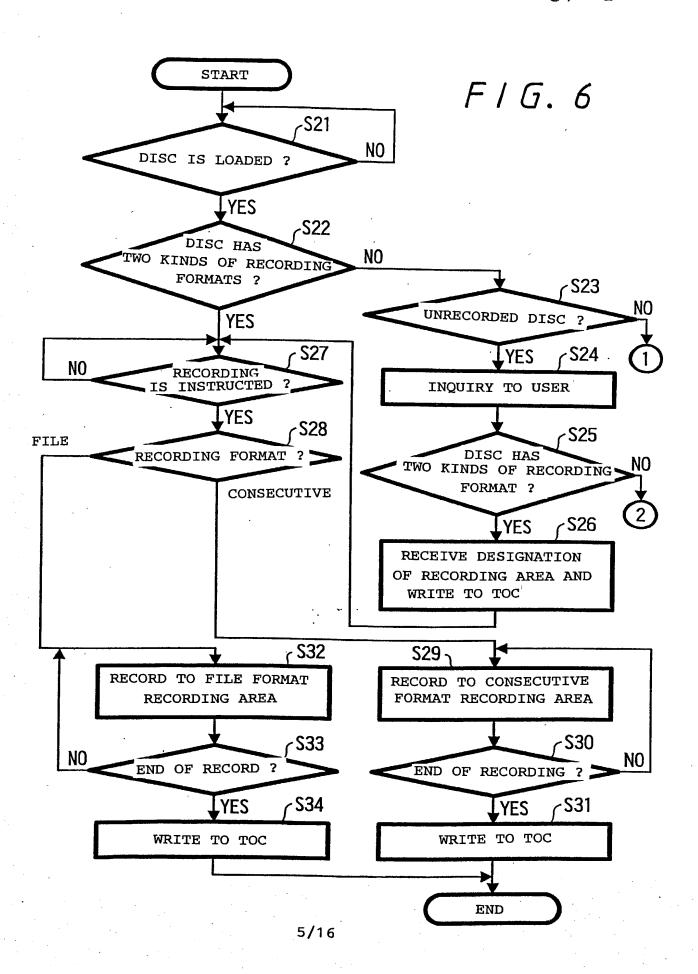


F / G. 4

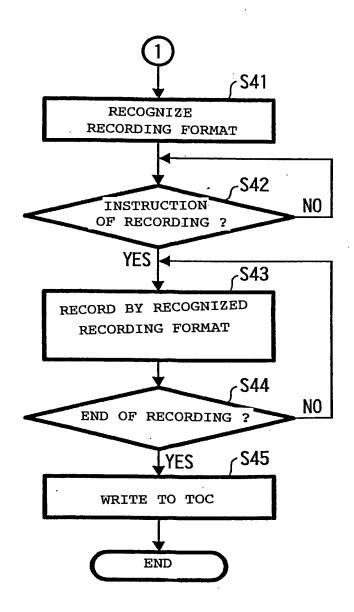




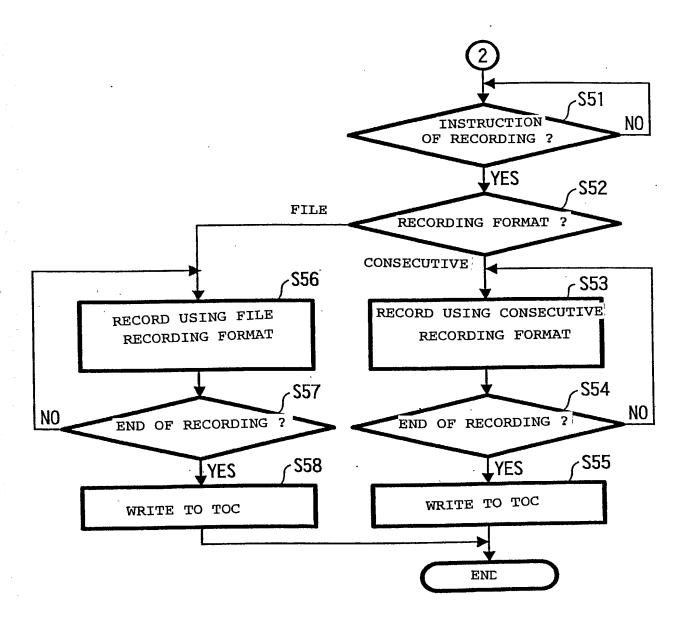


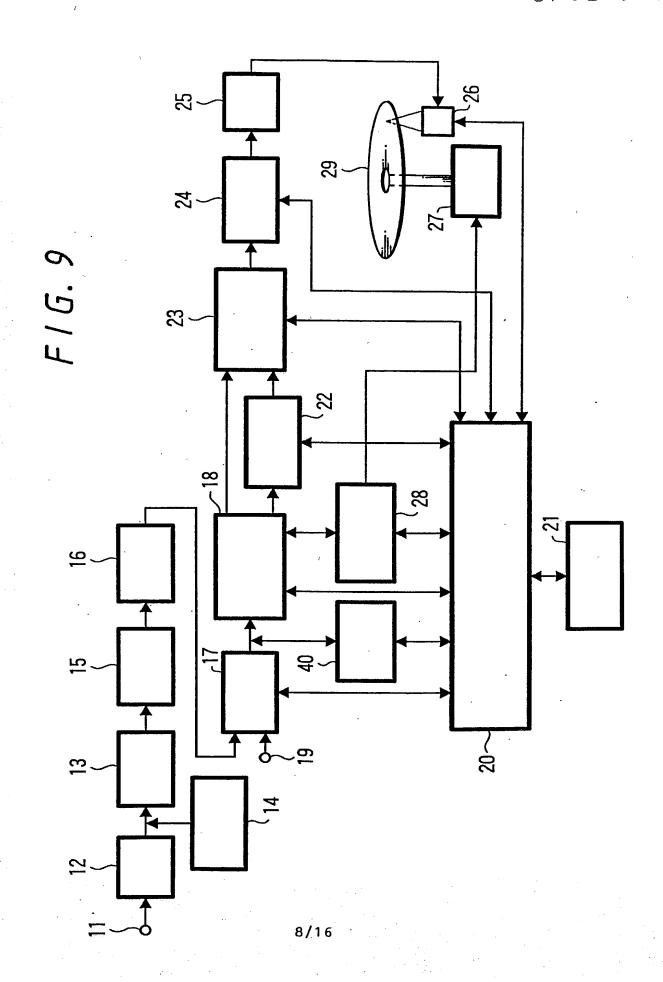


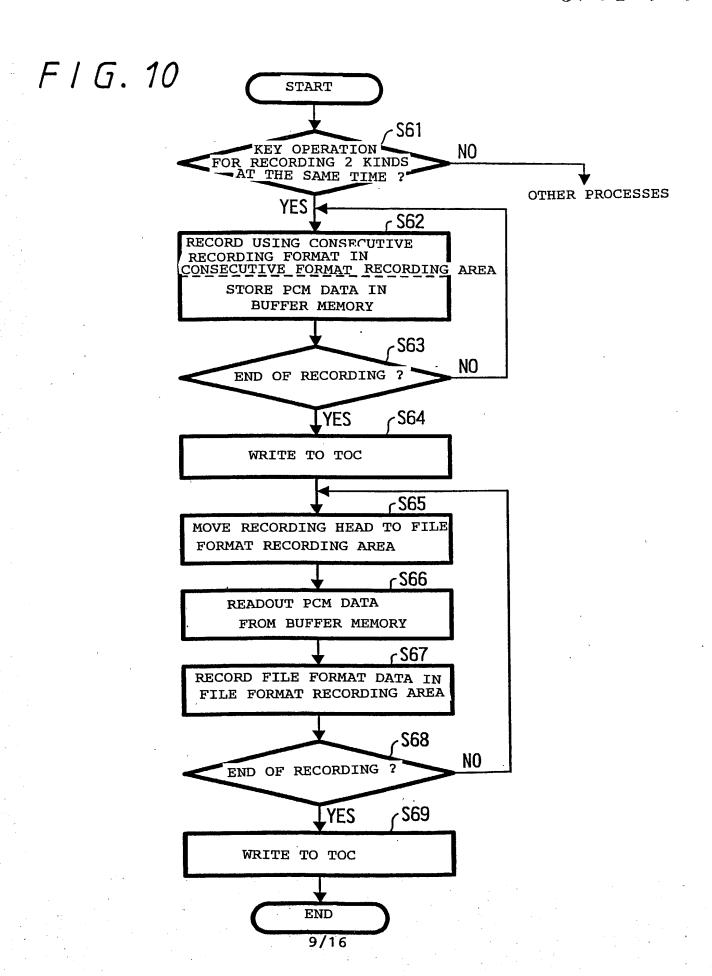
F / G. 7

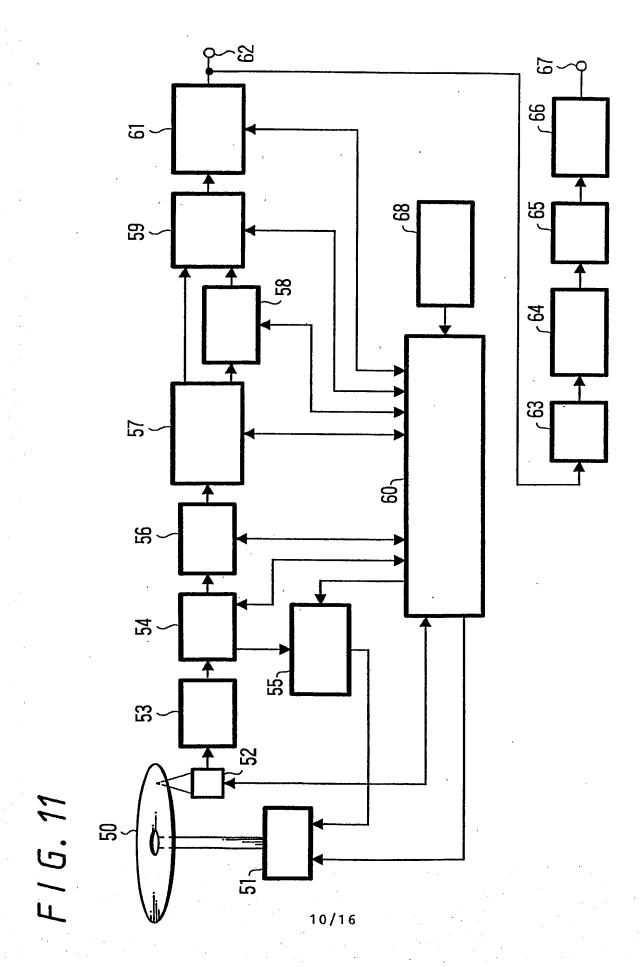


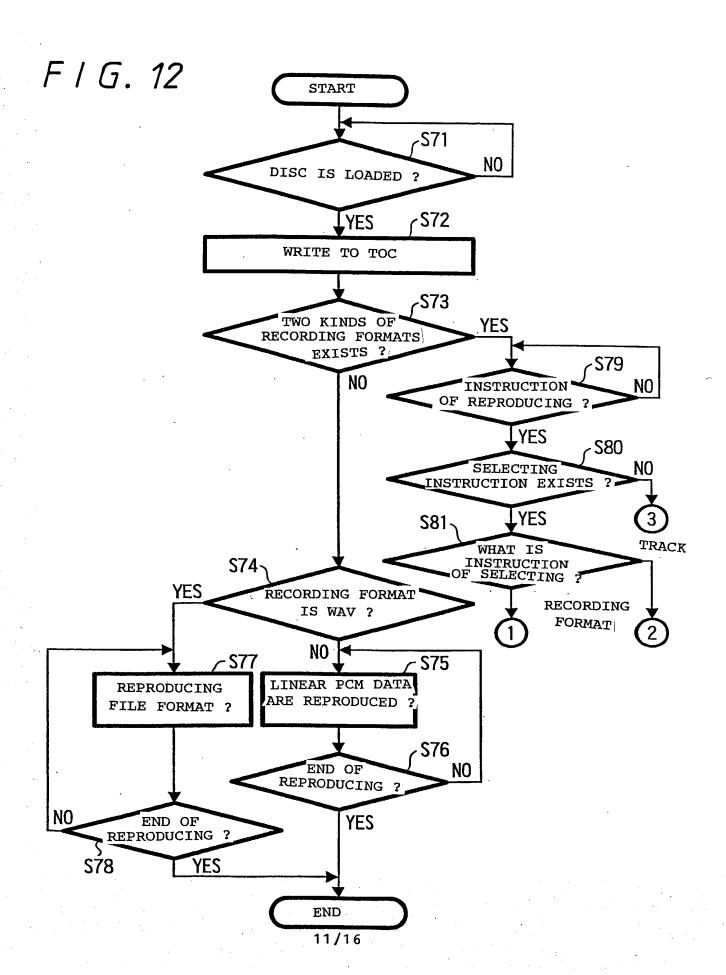
F 1 G. 8

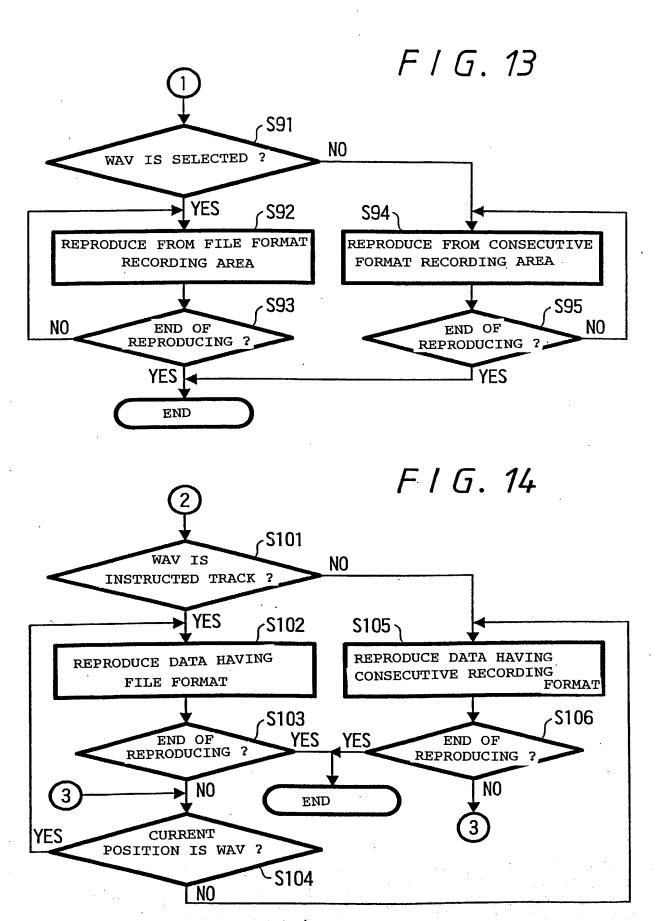






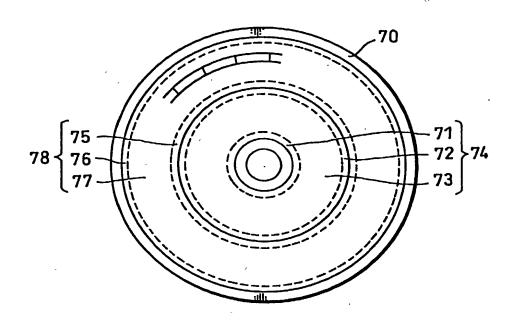


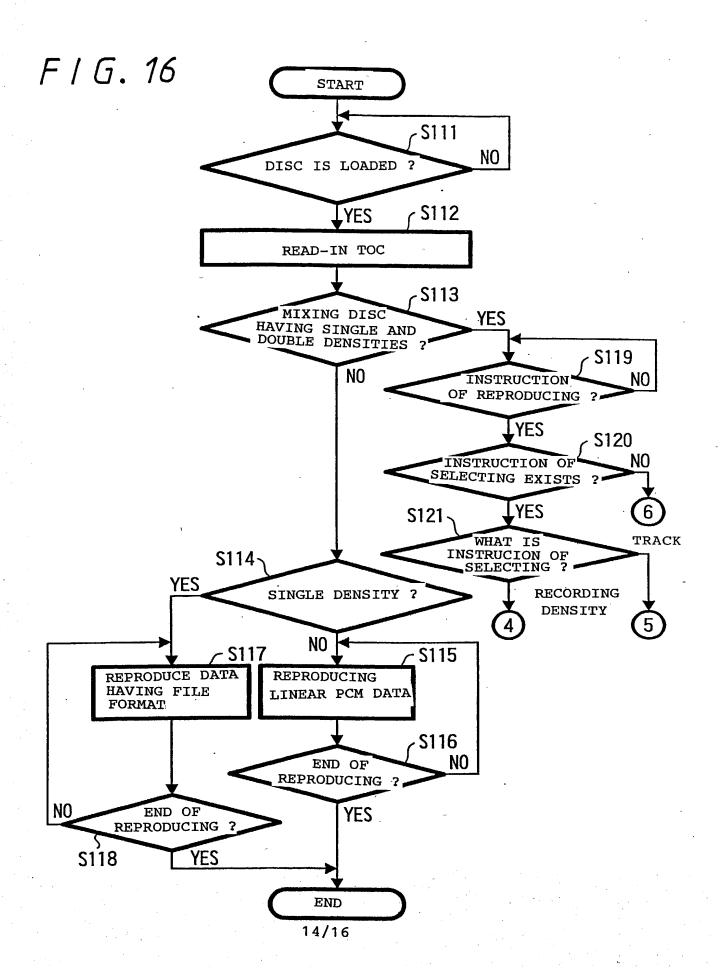


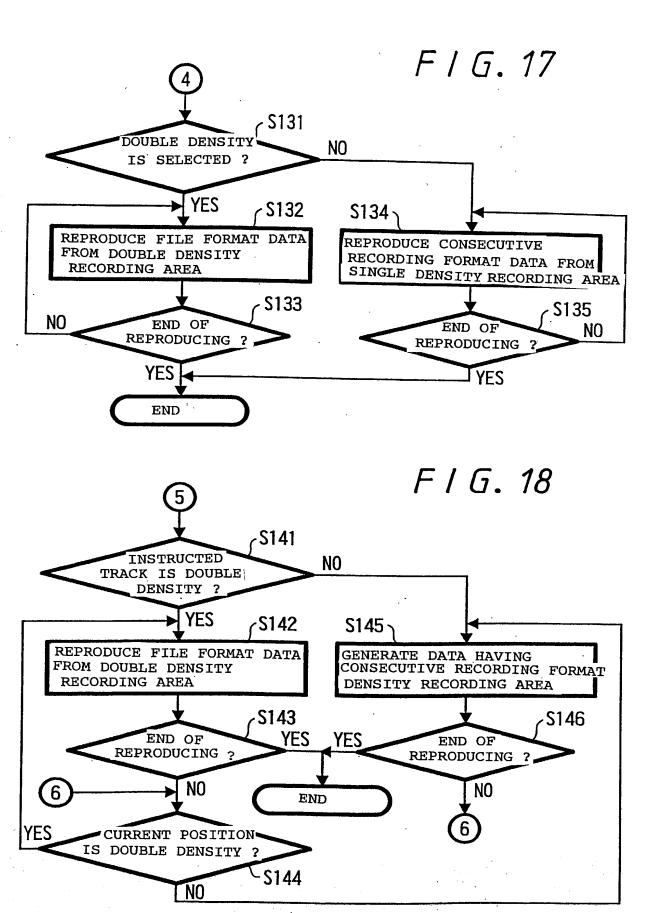


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F I G. 15







## DESCRIPTION OF REFERENCE NUMERALS

11	INPUT TERMINAL
12	LINE AMPLIFIER
13	LOW PASS FILTER
14	DITHER GENERATING CIRCUIT
15	
16	
17	INPUT SELECTOR
18	LINEAR PCM/WA SELECTOR
19	DIGITAL SIGNAL INPUT TERMINAL
20	SYSTEM CONTROLLER
21	KEY INPUT UNIT
22	
2,3	ECC ENCODER
24	
25	
26	
27	
28	SPEED CONTROL CIRCUIT
29	OPTICAL DISC